

WEST Search History

Hide Items

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DATE: Friday, June 10, 2005

09/825,654

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
		DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR	
<input type="checkbox"/>	L128	l125 and L127	5
<input type="checkbox"/>	L127	L123 same semaphore\$1	45
<input type="checkbox"/>	L126	l124 and L125	0
<input type="checkbox"/>	L125	l106 or l122 or l118 or l38 or l41 or l42	15698
<input type="checkbox"/>	L124	l47 same l123	4
<input type="checkbox"/>	L123	(register or l119) same latch	53567
<input type="checkbox"/>	L122	l97 or L121	10025
<input type="checkbox"/>	L121	712/220.ccls.	332
<input type="checkbox"/>	L120	L5 and L119	0
<input type="checkbox"/>	L119	hardware adj control adj point\$1	20
		DB=USPT; PLUR=YES; OP=OR	
<input type="checkbox"/>	L118	5367693.pn.	1
<input type="checkbox"/>	L117	5394551.pn.	1
		DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR	
<input type="checkbox"/>	L116	semaphore\$1 and L115	23
<input type="checkbox"/>	L115	lock\$3 same L113	266
<input type="checkbox"/>	L114	L110 and L113	36
<input type="checkbox"/>	L113	interrupt\$1 same register\$1 same interface\$1	6635
<input type="checkbox"/>	L112	interrupt adj register adj control adj interface\$1	0
<input type="checkbox"/>	L111	interface\$1 same L110	5
<input type="checkbox"/>	L110	interrupt adj service adj register\$1	64
<input type="checkbox"/>	L109	L24 and L106	3
<input type="checkbox"/>	L108	L90 and L106	44
<input type="checkbox"/>	L107	L93 and L106	0
<input type="checkbox"/>	L106	(714/39).ccls.	399
<input type="checkbox"/>	L105	L93 and L104	12
<input type="checkbox"/>	L104	L101 and L103	386
<input type="checkbox"/>	L103	register adj interface	1927
<input type="checkbox"/>	L102	L93 same L101	5
<input type="checkbox"/>	L101	dac or (digital same analog same converter\$1)	224970

INVENTOR

NAME

SEARCH

DONE ON

PALM

6-10-2005

ST-JL

DB=USPT; PLUR=YES; OP=OR

☐ L100 5931948.pn. 1

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

☐ L99 L90 and L98 28

☐ L98 L70 and L97 66

☐ L97 (714/25 |714/26 |714/27 |714/28 |714/29 |714/30 |714/31 |714/32 |714/33
|714/34 |714/35 |714/36 |714/37 |714/38 |714/39 |714/40 |714/41 |714/42 |714/43
|714/44 |714/45 |714/46 |714/47 |714/48 |714/49 |714/50 |714/51 |714/52 |714/53
|714/54 |714/55 |714/56 |714/57).ccls. 9698

☐ L96 L88 and L95 0

☐ L95 L72 and L94 150

☐ L94 interface\$1 same L93 150

☐ L93 access adj control adj register\$1 613

☐ L92 L19 and L91 15

☐ L91 L72 same L90 1041

☐ L90 access adj control 55799

☐ L89 L87 and L88 2

☐ L88 L18 or L41 or L42 5079

☐ L87 (control same lock\$3).ti. 17292

☐ L86 L84 and L85 0

☐ L85 latch\$3.ti. 65681

☐ L84 L72 and L83 142

☐ L83 L66 same address\$2 1741

☐ L82 L81 same L72 8

☐ L81 software adj lock\$3 597

DB=USPT; PLUR=YES; OP=OR

☐ L80 US-5630102-A.did. 1

☐ L79 5440747.pn. 1

☐ L78 US-5630102-A.did. 1

☐ L77 5515530.pn. 1

☐ L76 US-5630102-A.did. 1

☐ L75 US-5630102-A.did. 1

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

☐ L74 L73.ti. 6

☐ L73 lock\$3 same L72 1172

☐ L72 control adj register\$1 24922

☐ L71 L66 and L70 97

☐ L70 register\$1 same lock\$3 same access 5954

☐ L69 hardware same L66 674

<input type="checkbox"/>	L68	L65 and L66	0
<input type="checkbox"/>	L67	L65 same L66	0
<input type="checkbox"/>	L66	control adj point\$1	28212
<input type="checkbox"/>	L65	wrapper\$1 same lock\$1	1113
<input type="checkbox"/>	L64	L58 and L63	1
<input type="checkbox"/>	L63	latch same lock\$3 same object\$1	27200
<input type="checkbox"/>	L62	L47 and L61	0
<input type="checkbox"/>	L61	object\$1 and L60	53
<input type="checkbox"/>	L60	L58 and L55	65
<input type="checkbox"/>	L59	L57 and L58	0
<input type="checkbox"/>	L58	L18 or L41 or L42 or L38	5893
<input type="checkbox"/>	L57	L55 and L5	4
<input type="checkbox"/>	L56	L54 and L55	129797
<input type="checkbox"/>	L55	lock\$3 adj mechanism\$1	129800
<input type="checkbox"/>	L54	lock\$3 same mechanism	388725
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L53	5862052.pn.	1
<input type="checkbox"/>	L52	5916306.pn.	1
<input type="checkbox"/>	L51	5918233.pn.	1
<input type="checkbox"/>	L50	6028998.pn.	1
<input type="checkbox"/>	L49	6028998.pn.	1
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L48	L45 and L46 and L47	2
<input type="checkbox"/>	L47	latch adj object\$1	170
<input type="checkbox"/>	L46	access adj object\$1	6121
<input type="checkbox"/>	L45	hardware adj2 object\$1	783
<input type="checkbox"/>	L44	L23 and L43	0
<input type="checkbox"/>	L43	L41 and L42 and L38 and L18	0
<input type="checkbox"/>	L42	(713/100).ccls.	930
<input type="checkbox"/>	L41	(713/1 713/2).ccls.	2591
<input type="checkbox"/>	L40	orchestration adj object\$1	1
<input type="checkbox"/>	L39	L23 and L38	16
<input type="checkbox"/>	L38	(719/328).ccls.	963
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L37	6480597.pn.	1
<input type="checkbox"/>	L36	5325532.pn.	1
<input type="checkbox"/>	L35	5691897.pn.	1
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			

<input type="checkbox"/>	L34 L19 and L32	0
<input type="checkbox"/>	L33 L23 and L32	0
<input type="checkbox"/>	L32 control same latch same (layer\$1 and object\$1)	314
<input type="checkbox"/>	L31 L23 and L30	0
<input type="checkbox"/>	L30 control same latch same (layer\$1 or object\$1)	18030
	<i>DB=USPT; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L29 .5987614.pn.	1
<input type="checkbox"/>	L28 6185514.pn.	1
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L27 L23.ti.	3
	<i>DB=USPT; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L26 L18 and L25	6
<input type="checkbox"/>	L25 hal and L23	175
<input type="checkbox"/>	L24 hal or L23	17386
<input type="checkbox"/>	L23 Hardware adj abstraction adj layer\$1	274
<input type="checkbox"/>	L22 wrapper\$1 and L21	0
<input type="checkbox"/>	L21 firmware same L19	29
<input type="checkbox"/>	L20 L5 and L19	0
<input type="checkbox"/>	L19 control adj points	11524
<input type="checkbox"/>	L18 L7 or L8 or L9 or L10 or L11 or L12 or L13 or L14 or L15 or L16 or L17	2061
<input type="checkbox"/>	L17 (719/320).ccls.	88
<input type="checkbox"/>	L16 (719/319).ccls.	69
<input type="checkbox"/>	L15 (719/318).ccls.	290
<input type="checkbox"/>	L14 (719/317).ccls.	134
<input type="checkbox"/>	L13 (719/316).ccls.	300
<input type="checkbox"/>	L12 (719/315).ccls.	586
<input type="checkbox"/>	L11 (719/314).ccls.	99
<input type="checkbox"/>	L10 (719/313).ccls.	283
<input type="checkbox"/>	L9 (719/312).ccls.	105
<input type="checkbox"/>	L8 (719/311).ccls.	72
<input type="checkbox"/>	L7 (719/310).ccls.	402
<input type="checkbox"/>	L6 L5.ti.	1
<input type="checkbox"/>	L5 latch adj object\$1	42
<input type="checkbox"/>	L4 L1 and L2	0
<input type="checkbox"/>	L3 L1 and L2L2	0
<input type="checkbox"/>	L2 hardware adj control adj object\$1	2
<input type="checkbox"/>	L1 latch adj layer\$1	11

END OF SEARCH HISTORY